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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

#11

Applicants: Takahiro Yamaguchi, et al.  
Serial No.: 09/811,153  
Filing Date: March 16, 2001  
Title: Apparatus for and Method of  
Measuring Jitter

Examiner: Mohammad Ghayour

Art Unit: 2634

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Technology Center 2600

December 6, 2002  
San Francisco, California

Commissioner of Patents  
Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to the duty of disclosure set forth in 37 CFR § 1.56, Applicants respectfully submit the following information disclosure statement.

The undersigned certifies that the following reference was contained in a communication from a counterpart international application not more than three months prior to the filing of this statement. A copy of the International Search Report is attached.

The disclosed reference is:

Yamaguchi, T., et al., "Extraction of peak-to-peak and RMS sinusoidal jitter using an analytic signal method," Proceedings 18<sup>th</sup> IEEE VLSI Test Symposium, Montreal, Quebec, Canada, 4/30-5/4/2000, pages 395-402.

*Information Disclosure Statement Application No. 09/811,153*

Applicants respectfully request that the Examiner initial the cited references shown on the enclosed form PTO-1449 and that the references be made of record in the present application.

Respectfully submitted,



David N. Lathrop  
Reg. No. 34,655  
601 California St., Suite 1111  
San Francisco, CA 94108-2805  
Telephone: (415) 989-8080  
Facsimile: (415) 989-0910

**Certificate of Mailing Under 37 CFR 1.8**

I certify that this Information Disclosure Statement and any enclosed materials are being deposited with the United States Postal Service on December 6, 2002 with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231.



David N. Lathrop

Enc. Cited reference (1)  
PTO Form 1449  
Return receipt postcard